

REMARKS

Claims 1-9, 11-19, 21-27, 29, and 31-40 are pending in the present application. By this Response, claims 38-40 are added. No new matter has been added by the addition of claims 38-40. Reconsideration of the claims is respectfully requested in view of the following remarks.

I. Telephone Interview

Applicants thank Examiner Parthasarathy for the courtesies extended to Applicants' representative during the June 4, 2008 telephone interview. During the telephone interview, the above amendments and the distinctions of the claims over the cited art were discussed. Examiner Parthasarathy agreed that if it were specified in a dependent claim that, with regard to one illustrative embodiment, the at least one first controlled processor and the second controlled processor are synergistic processing units, that the rejection under 35 U.S.C. § 112, first paragraph would be overcome with regard to all of claims 1-9, 11-19, 21-27, 29 and 31-37. Accordingly, Applicants have added claims 38-40 to specifically recite one illustrative embodiment in which the at least one first controlled processor and the second controlled processor are synergistic processing units. The substance of the telephone interview is summarized in the following remarks.

II. Obviousness-Type Double Patenting Rejection

The Office Action maintains the rejection of claims 1-9, 11-19, 21-27, 29, and 31-37 on the ground of nonstatutory obviousness-type double patenting based on claims 1-24 of U.S. Patent No. 6,981,072. This rejection is respectfully traversed. While Applicants believe that this rejection is improper for the many reasons set forth in Applicants' Response filed January 30, 2008, in an effort to expedite prosecution of this application towards allowance, Applicants are filing herewith a Terminal Disclaimer as suggested by the Office Action. Accordingly, Applicants respectfully request withdrawal of the obviousness-type double patenting rejection of claims 1-9, 11-19, 21-27, 29, and 31-37.

III. Rejection under 35 U.S.C. 112, First Paragraph

The Office Action maintains the rejection of claims 1-9, 11-19, 21-27, 29 and 31-37 under 35 U.S.C. § 112, first paragraph. This rejection is respectfully traversed for the reasons discussed during the June 4, 2008 telephone interview as agreed by Examiner Parthasarathy.

Specifically, during the telephone interview, Examiner Parthasarathy agreed that, in the exemplary embodiment set forth in the specification, the PU is a control processor and the SPUs are controlled processors. Thus, Examiner Parthasarathy agreed that the specification provides support for the features of a control processor and a controlled processor. However, Examiner Parthasarathy requested that it be specified in a dependent claim that, with regard to one exemplary embodiment, the at least one first controlled processor and the second controlled processor are synergistic processing units. Accordingly, Applicants have added claims 38-40 by this Response to specifically set forth an embodiment in which the at least one first controlled processor and the second controlled processor are synergistic processing units.

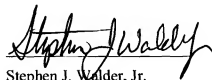
Thus, it is Applicants' understanding that the rejection under 35 U.S.C. § 112, first paragraph has been overcome as agreed upon by the Examiner. Accordingly, Applicants respectfully request withdrawal of the rejection under 35 U.S.C. § 112, first paragraph.

IV. Conclusion

It is respectfully urged that the subject application is now in condition for allowance. The Examiner is invited to call the undersigned at the below-listed telephone number if in the opinion of the Examiner such a telephone conference would expedite or aid the prosecution and examination of this application.

Respectfully submitted,

DATE: July 10, 2008

A handwritten signature in black ink, appearing to read "Stephen J. Walder, Jr.", written over a horizontal line.

Stephen J. Walder, Jr.

Reg. No. 41,534

WALDER INTELLECTUAL PROPERTY LAW, P.C.

P.O. Box 832745

Richardson, TX 75083

(214) 722-6419

ATTORNEY FOR APPLICANTS